

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Rapport, et al.	Art Unit	: 2841
Serial No.	: 10/804,452	Examiner	: Andargie M Aychillhum
Filed	: March 19, 2004	Conf. No.	: 7279
Title	: MEMORY EXPANSION AND INTEGRATED CIRCUIT STACKING SYSTEM AND METHOD		

**MAIL STOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO FINAL OFFICE ACTION OF JANUARY 31, 2008

Please amend the above-identified application as follows:

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 6 of this paper.